

EL 844046555

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 08/996,325
Priority Filing Date December 22, 1997
Inventor H. Montgomery Manning
Assignee Micron Technology, Inc.
Priority Group Art Unit 2822
Priority Examiner M. Trinh
Attorney's Docket No. MI22-1698
Title: Methods of Forming Thin Film Transistors



Stewart
dy
12-10-01

INFORMATION DISCLOSURE STATEMENT

References - - See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

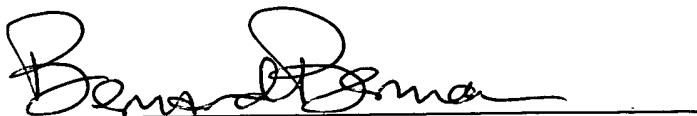
The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a divisional application of co-pending Application Serial No. 08/996,325, filed December 22, 1997, upon which the above-identified application relies for a priority dated under 35 U.S.C. §120. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. §1.98(d) and MPEP §609(2). As a courtesy, Applicant submits copies of the cited references for review.

Citation of these references is respectfully requested.

Respectfully submitted,

Date:

Aug 1, 2001

A handwritten signature in black ink, appearing to read "Bernard Berman", written over a horizontal line.

Bernard Berman

Reg. No. 37,279

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Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-1698	SERIAL NO. Filed herewith
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT H. Montgomery Manning	
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U.S. PATENT DOCUMENTS							
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
AA	4,243,997	01/06/81	Natori et al.				
AB	4,467,518	08/28/84	Bansal et al.				
AC	4,845,537	07/04/89	Nishimura et al.				
AD	4,864,374	09/05/89	Banerjee				
AE	5,001,540	03/19/91	Ishihara				
AF	5,156,987	10/20/92	Sandhu et al.				
AG	5,208,172	05/04/93	Fitch et al.				
AH	5,214,295	05/25/93	Manning				
AI	5,229,310	07/20/93	Sivan				
AJ	5,270,968	12/14/93	Kim et al.				
AK	5,274,259	12/28/93	Grabowski et al.				

FOREIGN PATENT DOCUMENTS							
Document Number	Date	Country	Class	Subclass	Translation		
					Yes	No	
AL							
AM							
AN							
AO							
AP							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)		
AR		"A New Toroidal TFT Structure for Future Generation SRAMs"; J. D. Hayden et al.; 1993 IEEE; pp. 33.5.1-33.5.4
AS		"Methods of Forming Small Contact Holes"; IBM Technical Disclosure Bulletin; January 1988; Vol 30 No. 8; pp. 252-253
AT		

EXAMINER	DATE CONSIDERED
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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U.S. PATENT DOCUMENTS							
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	5,283,455	Inoue et al.				
	AB	5,334,862	Manning et al.				
	AC	5,397,731	Takemura				
	AD	5,418,393	Hayden				
	AE	5,432,370	Kitamura et al.				
	AF	5,444,275	Kugishima et al.				
	AG	5,463,240	Watanabe				
	AH	5,508,531	Ha				
	AI	5,561,308	Kamata et al.				
	AJ	5,599,724	Yoshida				
	AK	5,612,546	Choi et al.				

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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	5,640,034	06/17/97	Malhi			
	AB	5,700,727	12/23/97	Manning			
	AC	5,747,359	05/05/98	Yuan et al.			
	AD	5,925,894	07/20/99	Yang			
	AE	5,994,735	11/30/99	Maeda et al.			
	AF	6,175,134	01/16/01	Manning			
	AG						
	AH						
	AI						
	AJ						
	AK						

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